Vishay High Power Products

ADD-A-PAK Generation VII **Power Modules Schottky Rectifier, 400 A**

FEATURES

- 175 °C T_{.1} operation
- · Low forward voltage drop
- · High frequency operation
- · Low thermal resistance
- UL pending
- · Compliant to RoHS directive 2002/95/EC
- · Designed and qualified for industrial level

BENEFITS

- · Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- · High surge capability
- · Easy mounting on heatsink

ELECTRICAL DESCRIPTION

The VSKCS403.. Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	400	А		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	25 500	А		
V _F	200 Apk, T _J = 125 °C	0.83	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VSKCS403/100	UNITS	
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V _{RWM}	100	V	

For technical questions, contact: indmodules@vishay.com

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ADD-A-PAK

PRODUCT SUMMARY				
I _{F(AV)}	400 A			

MECHANICAL DESCRIPTION

The ADD-A-PAK generation VII, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.





COMPLIANT



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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T _C = 111 °C	rootongular wavoform	400	
forward current	per leg	I _{F(AV)}	50 % duty cycle at T _C = TTT C	, rectangular wavelonn	200	
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	25 500	A
non-repetitive surge current		I _{FSM}	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	3300	
Non-repetitive avalanche energy		E _{AS}	T _J = 25 °C, I _{AS} = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		200 A	- T _J = 25 °C	0.99	V
Maximum forward voltage drop	V	400 A		1.3	
Maximum forward voltage drop	V _{FM}	200 A	- T _J = 125 °C	0.83	
		400 A		1.09	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C	V _R = Rated V _R	6	mA
Maximum reverse leakage current		T _J = 125 °C		80	ma
Maximum junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5500	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
Maximum RMS insulation voltage	V _{INS}	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.26	°C/W
Typical thermal resistance, case to heatsink per module		R _{thCS}		0.1	0/11
Approximate weight				75	g
				2.7	oz.
Mounting torque ± 10 %	to heatsink		A mounting compound is recommended and the torque should be rechecked after a period of 3 h to allow for the	4	Nm
	busbar		spread of the compound.	3	INIII
Case style			JEDEC	TO-240AA co	ompatible

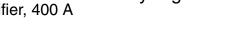


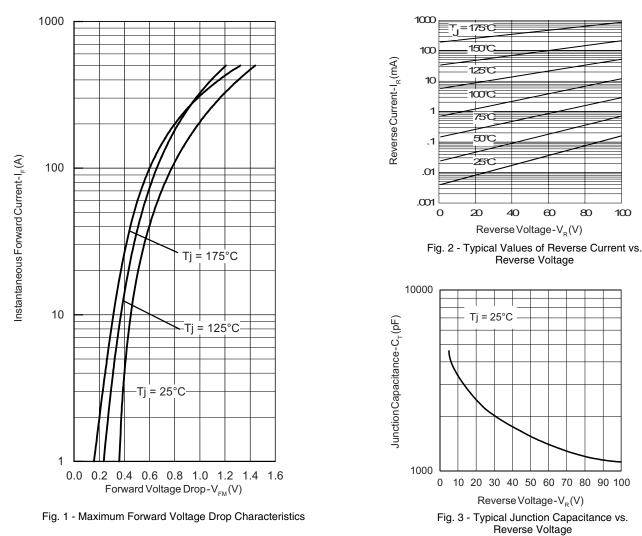
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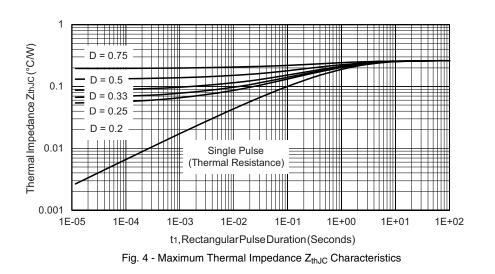
60

80

100

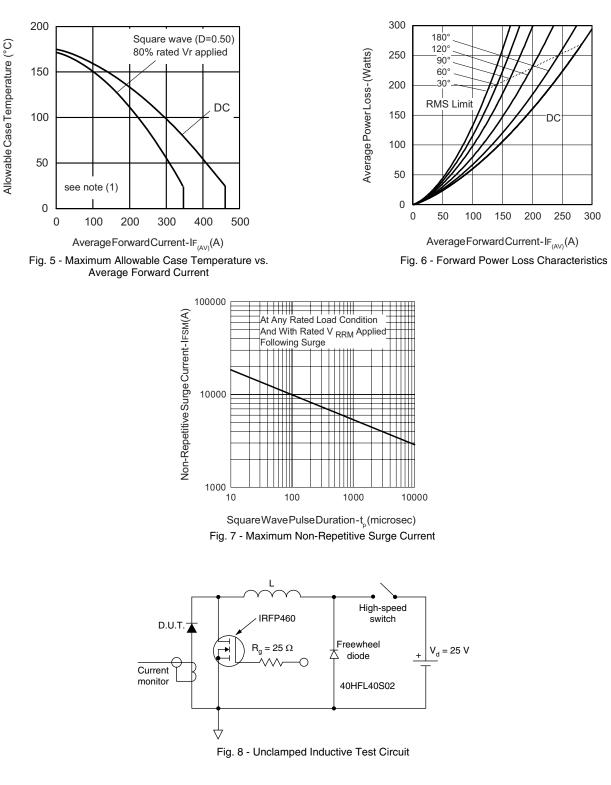






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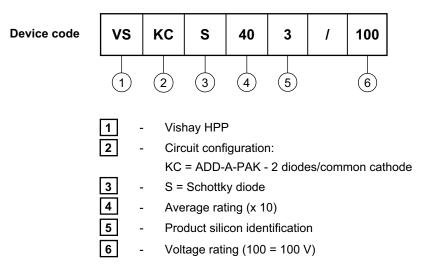
⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$ $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D); I_R at V_{R1} = 80 \% rated V_R$

Note

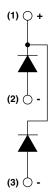


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ORDERING INFORMATION TABLE



CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95369		



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